



Bangabandhu Sheikh Mujibur Rahman University, Kishoreganj

Department of Computer Science and Engineering

Class Routine for Second Year First Semester (Academic Session: 2021-22)

Effective from: 11th February 2024

Classroom
#404

Time Day	08:30AM - 09:50AM	10:00AM - 11:20AM	11:30AM - 12:50PM	12:50AM - 02:00PM	02:00PM - 03:20PM	03:30PM - 04:50PM
SUN			In-course Examination		EEE 2106: Electronics Lab (NT & MSZ) Circuit Lab, 5th Floor	
MON	EEE 2109: Electronics (MAH)	STA 2107: Probability and Statistics (MEH)	CSE 2103: Data Structures and Algorithms-I (SB)		CSE 2104: Data Structure and Algorithms-I Lab (SB & NT) Computer Lab, 5th Floor	
TUES	STA 2107: Probability and Statistics (MEH)	CSE 2102: Object Oriented Lab (AAB & MSZ) Computer Lab, 5th Floor			CSE 2101: Object Oriented Programming (AAB)	CSE 2105: Computer Architecture (MSZ)
WED	EEE 2109: Electronics (MAH)	CSE 2101: Object Oriented Programming (AAB)	CSE 2105: Computer Architecture (MSZ)		CSE 2103: Data Structures and Algorithms-I (SB)	
THURS						

MEH: Md. Ershadul Haque, **MAH:** Dr. Md. Ahsan Habib, **SB:** Swapnil Biswas, **AAB:** Al Amin Biswas, **NT:** Ms. Noshin Tahsin; **MSZ:** Mr. Md. Sabab Zulfiker, **MSI:** Md. Saiful Islam;

Events	Tentative Dates
Start of Class	11th February 2024
1st In-course Examination	13th March 2024 to 27th March 2024
2nd In-course Examination	2nd May 2024 to 9th May 2024
End of Class	30th May 2024
Start of Final Examination	27th June 2024



Bangabandhu Sheikh Mujibur Rahman University, Kishoreganj

Department of Computer Science and Engineering

Class Routine for First Year Second Semester (Academic Session: 2022-23)

Effective from: 21st April 2024

Classroom
#403

Time Day	08:30AM - 09:50AM	10:00AM - 11:20AM	11:30AM - 12:50PM	12:50AM - 02:00PM	02:00PM - 03:20PM	03:30PM - 04:50PM
SUN		In-course Examination	MAT 1205: Linear Algebra (MYA)	BREAK	GED 1209: Bangladesh Studies (SM)	GED 1209: Bangladesh Studies (SM)
MON	CSE 1202: Digital Logic Design Lab (SB & MSZ) Circuit Lab, 5th Floor		MAT 1205: Linear Algebra (MYA)			
TUES		CSE 1204: Structured Programming Lab (SB & NT) Computer Lab, 8th Floor				CSE 1201: Digital Logic Design (AAB)
WED	CSE 1203: Structured Programming (NT)	EEE 1207: Electrical Circuits (MAH)	CSE 1201: Digital Logic Design (AAB)		EEE 1206: Electrical Circuits Lab (AAB & MSZ) Circuit Lab, 5th Floor	
THURS	EEE 1207: Electrical Circuits (MAH)	CSE 1203: Structured Programming (NT)				

SM: Dr. Shantanu Majumder, **MAH:** Dr. Md. Ahsan Habib, **SB:** Mr. Swapnil Biswas, **AAB:** Mr. Al Amin Biswas, **NT:** Ms. Noshin Tahsin; **MSZ:** Mr. Md. Sabab Zulfiker, **MYA:** Md. Mashih Ibn Yasin Adan;

Events	Tentative Dates
Start of Class	21st April 2024
1st In-course Examination	1st June 2024 to 10th July 2024
2nd In-course Examination	1st August 2024 to 29th August 2024
End of Class	End of August 2024
Start of Final Examination	15th September 2024